

Note: This schematic only shows the critical components and traces for a minimum footprint layout. ENABLE, Vout-programming, and other small-signal pins need to be connected and routed according to the customer application.

| Revision | Description         | Date | Approved |
|----------|---------------------|------|----------|
|          | Preliminary release |      |          |
|          |                     |      |          |
|          |                     |      |          |
|          |                     |      |          |

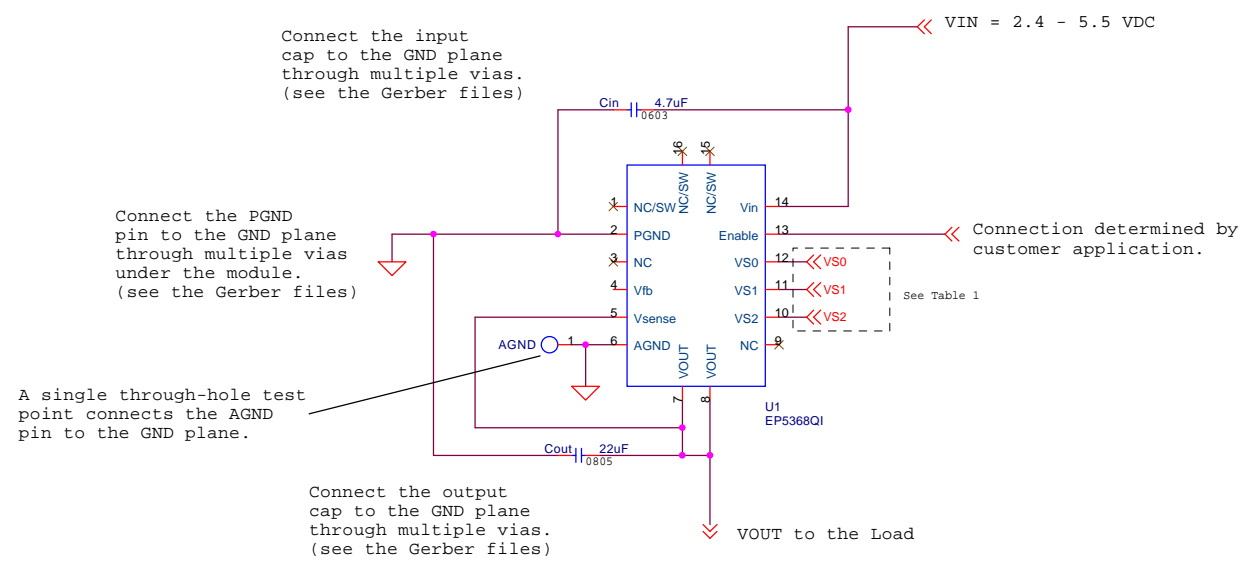



Table 1

| Voltage Programming Connections |     |     |                   |  |
|---------------------------------|-----|-----|-------------------|--|
| Voltage Selector                |     |     | Output Voltage    |  |
| VS2                             | VS1 | VS0 | VOUT              |  |
| 0                               | 0   | 0   | 3.3V              |  |
| 0                               | 0   | 1   | 2.5V              |  |
| 0                               | 1   | 0   | 1.8V              |  |
| 0                               | 1   | 1   | 1.5V              |  |
| 1                               | 0   | 0   | 1.25V             |  |
| 1                               | 0   | 1   | 1.2V              |  |
| 1                               | 1   | 0   | 0.8V              |  |
| 1                               | 1   | 1   | External Divider* |  |

0 = Connect to the GND plane.  
1 = Leave open. Pulled high inside the device.  
\* Not shown. Refer to the datasheet.

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|   |                |                       |     |
|---|----------------|-----------------------|-----|
| Title<br>EP5368QI Critical Components Ckt for Minimum Footprint |                |                       |     |
| Size  | Drawing Number | Drawn By<br>C. Romano | Rev |
| Date  | May 1, 2008    |                       |     |
|   | Sheet          | of                    |     |